

## PALM INTRANET

Day : Tuesday  
Date: 5/31/2005  
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## Inventor Name Search Result

Your Search was:

Last Name = HARTWELL

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 40
<u>60229830</u>	Not Issued	159	08/31/2000	SYMMETRICAL MULTIPROCESS COMPUTER SYSTEM	HARTWELL, DAVID
<u>10750495</u>	Not Issued	030	12/31/2003	RESTORING ACCESS TO A FAILED DATA STORAGE DEVICE IN A REDUNDANT MEMORY SYSTEM	HARTWELL, DAVID W.
<u>10677583</u>	Not Issued	030	10/02/2003	SCALABLE EFFICIENT I/O PORT PROTOCOL	HARTWELL, DAVID W.
<u>10649523</u>	Not Issued	041	08/27/2003	CLOCK FORWARD INITIALIZATION AND RESET SIGNALING TECHNIQUE	HARTWELL, DAVID
<u>10068427</u>	<u>6826653</u>	150	02/06/2002	BLOCK DATA MOVER ADAPTED TO CONTAIN FAULTS IN A PARTITIONED MULTIPROCESSOR SYSTEM	HARTWELL, DAVID W.
<u>09944516</u>	Not Issued	094	08/31/2001	ANTI-STARVATION INTERRUPT PROTOCOL	HARTWELL, DAVID W.
<u>09944500</u>	Not Issued	071	08/31/2001	DETECTION OF ADDED OR MISSING FORWARDING DATA CLOCK SIGNALS	HARTWELL, DAVID W.
<u>09653643</u>	<u>6668335</u>	150	08/31/2000	SYSTEM FOR RECOVERING DATA IN A MULTIPROCESSOR SYSTEM COMPRISING A CONDUCTION PATH FOR EACH BIT BETWEEN PROCESSORS WHERE THE PATHS ARE GROUPED INTO SEPARATE BUNDLES AND ROUTED ALONG DIFFERENT PATHS	HARTWELL, DAVID W.
<u>09652980</u>	<u>6629257</u>	150	08/31/2000	SYSTEM AND METHOD TO AUTOMATICALLY RESET AND INITIALIZE A CLOCKING SUBSYSTEM WITH RESET SIGNALING TECHNIQUE	HARTWELL, DAVID
<u>09652645</u>	<u>6724850</u>	150	08/31/2000	DETERMINISTIC HARDWARE BEHAVIOR BETWEEN MULTIPLE ASYNCHRONOUS CLOCK DOMAINS THROUGH THE	HARTWELL, DAVID

				NOVEL USE OF A PLL	
<u>09652644</u>	6701387	150	08/31/2000	ADAPTIVE DATA FETCH PREDICTION ALGORITHM	HARTWELL, DAVID W.
<u>09652563</u>	Not Issued	160	08/31/2000	ENHANCED CLOCK FORWARDING DATA RECOVERY	HARTWELL, DAVID
<u>09652494</u>	6640272	150	08/31/2000	AUTOMATED BACKPLANE CABLE CONNECTION IDENTIFICATION SYSTEM AND METHOD	HARTWELL, DAVID
<u>09652459</u>	Not Issued	094	08/31/2000	ENHANCED CLOCK FORWARDING DATA RECOVERY	HARTWELL, DAVID
<u>09652391</u>	6738836	150	08/31/2000	SCALABLE EFFICIENT I/O PORT PROTOCOL	HARTWELL, DAVID W.
<u>09651531</u>	Not Issued	160	08/29/2000	AUTOMATED BACKPLANE CABLE CONNECTION IDENTIFICATION SYSTEM AND METHOD	HARTWELL, DAVID
<u>09292658</u>	Not Issued	161	04/15/1999	GRAPHICAL NETWORK SEARCH ENVIRONMENT	HARTWELL, DAVID CHARLES
<u>08869610</u>	6076129	150	06/06/1997	DISTRIBUTED DATA BUS SEQUENCING FOR A SYSTEM BUS WITH SEPARATE ADDRESS AND DATA BUS PROTOCOLS	HARTWELL, DAVID W.
<u>08687692</u>	5652861	150	07/26/1996	METHOD OF MEMORY INTERLEAVING, AND MEMORY SYSTEMS INTERLEAVED THEREBY	HARTWELL, DAVID W.
<u>08590802</u>	5666551	150	01/24/1996	DISTRIBUTED DATA BUS SEQUENCING FOR A SYSTEM BUS WITH SEPARATE ADDRESS AND DATA BUS PROTOCOLS	HARTWELL, DAVID W.
<u>08382953</u>	5544179	150	02/01/1995	MIS-SYNCHRONIZATION DETECTION SYSTEM USING A COMBINED ERROR CORRECTING AND CYCLE IDENTIFIER CODE	HARTWELL, DAVID
<u>08270297</u>	Not Issued	166	06/30/1994	DISTRIBUTED DATA BUS SEQUENCING FOR A SYSTEM BUS WITH SEPARATE ADDRESS AND DATA BUS PROTOCOLS	HARTWELL, DAVID W.
<u>08269234</u>	6360285	150	06/30/1994	APPARATUS FOR DETERMINING MEMORY BANK AVAILABILITY IN A COMPUTER SYSTEM	HARTWELL, DAVID
<u>08248032</u>	Not Issued	161	05/24/1994	DYNAMIC ARBITRATION FOR SYSTEM BUS IN MULTIPROCESSOR COMPUTER SYSTEM	HARTWELL, DAVID W.

<u>08219807</u>	<u>5388222</u>	150	03/28/1994	MEMORY SUBSYSTEM COMMAND INPUT QUEUE HAVING STATUS LOCATIONS FOR RESOLVING CONFLICTS	HARTWELL, DAVID W.
<u>08110634</u>	Not Issued	166	08/23/1993	DYNAMIC ARBITRATION FOR SYSTEM BUS IN MULTIPROCESSOR COMPUTER SYSTEM	HARTWELL, DAVID W.
<u>08023033</u>	Not Issued	166	02/24/1993	SYSTEM FOR INTERLEAVING MEMORY MODULES AND BANKS	HARTWELL, DAVID W.
<u>07875564</u>	Not Issued	161	04/27/1992	REUSABLE CONTAINER	HARTWELL, DAVID
<u>07870448</u>	<u>5412788</u>	150	04/16/1992	MEMORY BANK MANAGEMENT AND ARBITRATION IN MULTIPROCESSOR COMPUTER SYSTEM	HARTWELL, DAVID W.
<u>07870436</u>	Not Issued	166	04/16/1992	DYNAMIC ARBITRATION FOR SYSTEM BUS CONTROL IN MULTIPROCESSOR COMPUTER SYSTEM	HARTWELL, DAVID W.
<u>07870134</u>	<u>5301283</u>	150	04/16/1992	DYNAMIC ARBITRATION FOR SYSTEM BUS CONTROL IN MULTIPROCESSOR DATA PROCESSING SYSTEM	HARTWELL, DAVID
<u>07860534</u>	Not Issued	166	03/30/1992	MIS-SYNCHRONIZATION DETECTION SYSTEM	HARTWELL, DAVID
<u>07765417</u>	Not Issued	166	09/25/1991	MEMORY SUBSYSTEM INPUT QUEUE	HARTWELL, DAVID W.
<u>07606880</u>	Not Issued	166	10/31/1990	REUSABLE CONTAINER	HARTWELL, DAVID
<u>07435397</u>	<u>5088419</u>	150	11/13/1989	TABLES	HARTWELL, DAVID W.
<u>07093488</u>	<u>4864496</u>	150	09/04/1987	BUS ADAPTER MODULE FOR INTERCONNECTING BUSSES IN A MULTIBUS COMPUTER SYSTEM	HARTWELL, DAVID W.
<u>07093480</u>	<u>4837767</u>	150	09/04/1987	BUS ADAPTER MODULE WITH IMPROVED ERROR RECOVERY IN A MULTIBUS COMPUTER SYSTEM	HARTWELL, DAVID W.
<u>07093479</u>	<u>4979097</u>	150	09/04/1987	METHOD AND APPARATUS FOR INTERCONNECTING BUSSES IN A MULTIBUS COMPUTER SYSTEM	HARTWELL, DAVID W.
<u>07093476</u>	<u>4858234</u>	150	09/04/1987	METHOD AND APPARATUS FOR ERROR RECOVERY IN A MULTIBUS COMPUTER SYSTEM	HARTWELL, DAVID W.

06040674	4244065	150	05/21/1979	WATER BED CONSTRUCTION	HARTWELL, DAVID
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Inventor Search Completed: No Records to Display.

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	HARTWELL	DAVID	

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